

## HIPStR – Heterogeneous-ISA Program State Relocation



## Ashish Venkat Sriskanda Shamasunder Hovav Shacham Dean Tullsen University of California, San Diego

## Return-Oriented Programming Buffer Overflowed Stack Dynamic Execution Stream Dynamic Executio

## Harnessing ISA Diversity: Escape from ROP ROP thrives on 4 fundamental characteristics: Ability to hijack control flow Prior knowledge of gadget locations Requires program state (registers/memory) to perform computation Knowledge of the underlying ISA **Massive Attack Surface Brute Force Attacks Reduction (99.09%)** computationally infeasible on even future Exascale **Processors** Removes one of the last Outperforms JIT-ROP remaining "constants" **Competition by 15.6%** available to the attacker knowledge of the ISA Heterogeneous-ISA Program State Relocation across heterogeneous ISAs Migration Runtime **DBT Engine DBT Engine** to perform to perform **Program State Relocation Program State Program State Relocation Program State** within each ISA within each ISA Phase Change High-Performance x86 Core Low-Power ARM Core Synergistically combines two strong and independent defense techniques: Binary Translation driven Program State Relocation Non-deterministic Execution Migration across Heterogeneous-ISAs





